Talbot et al.
Application No.: 09/662,068
Page 4

PATENT

65. (NEW) The method of claim 64 wherein said plurality of memory access requests comprises at least three memory access requests.

66. (NEW) The method of claim 64 further comprising an execution unit for executing said plurality of memory access requests in said execution order.

67. (NEW) The method of claim 66 further comprising a results ordering module for returning results of said execution unit according to said receiving order.
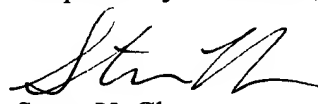
REMARKS

Claims 1 to 52 are pending. Applicant has deleted claims 1 – 52 without prejudice and added new claims 53 - 67.

CONCLUSION

Applicants believe all claims now pending in this Application are in condition for allowance. The issuance of a formal Notice of Allowance at an early date is respectfully requested.

If the Examiner believes a telephone conference would expedite prosecution of this application, please telephone the undersigned at 650-326-2400.

Respectfully submitted,

Steve Y. Cho
Reg. No. 44,612

TOWNSEND and TOWNSEND and CREW LLP
Two Embarcadero Center, 8th Floor
San Francisco, California 94111-3834
Tel: (415) 576-0200
Fax: (415) 576-0300
KK:amc
PA 3119725 v1

RECEIVED
JAN 23 2001
TC 2100 MAILROOM

OIPE
JAN 12 2001
PATENT & TRADEMARK OFFICE